

**Syst m and Method for
Manipulating Data with a Plurality of Processors**

ABSTRACT

A system and a method for sharing a common system
5 memory by a main processor and a plurality of secondary
processors. The sharing of the common system memory
enables the sharing of data between the processors. The
data are loaded into the common memory by the main
processor, which divides the data to be processed into data
10 blocks. The size of the data blocks is equal to the size
of the registers of the secondary processors. The main
processor identifies an available secondary processor to
process the first data block. The secondary processor
processes the data block and returns the processed data
15 block to the common system memory. The main processor may
continue identifying available secondary processors and
requesting the available secondary processors to process
data blocks until all the data blocks have been processed.